

FIG.1
PRIOR ART

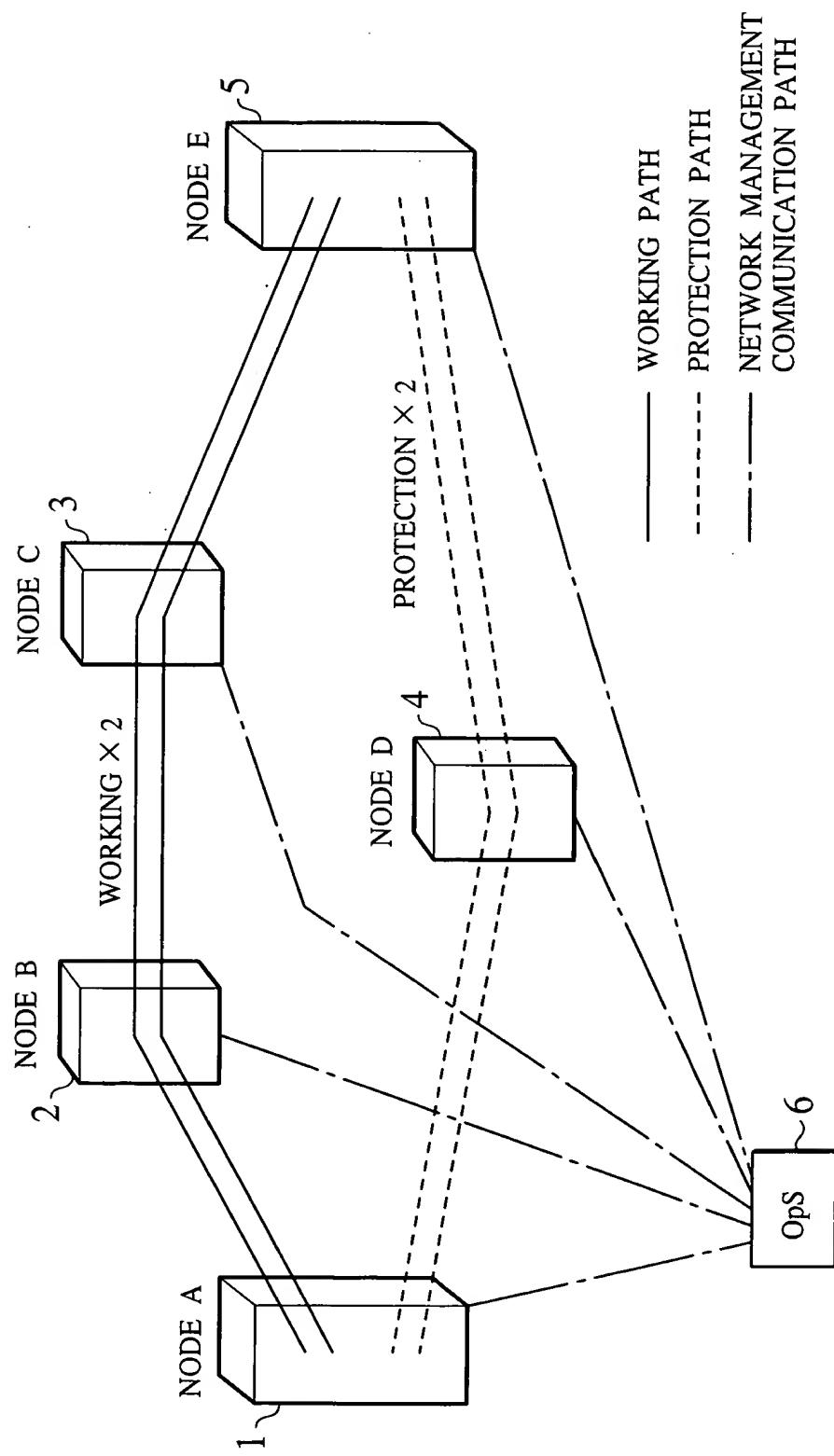


FIG.2
PRIOR ART

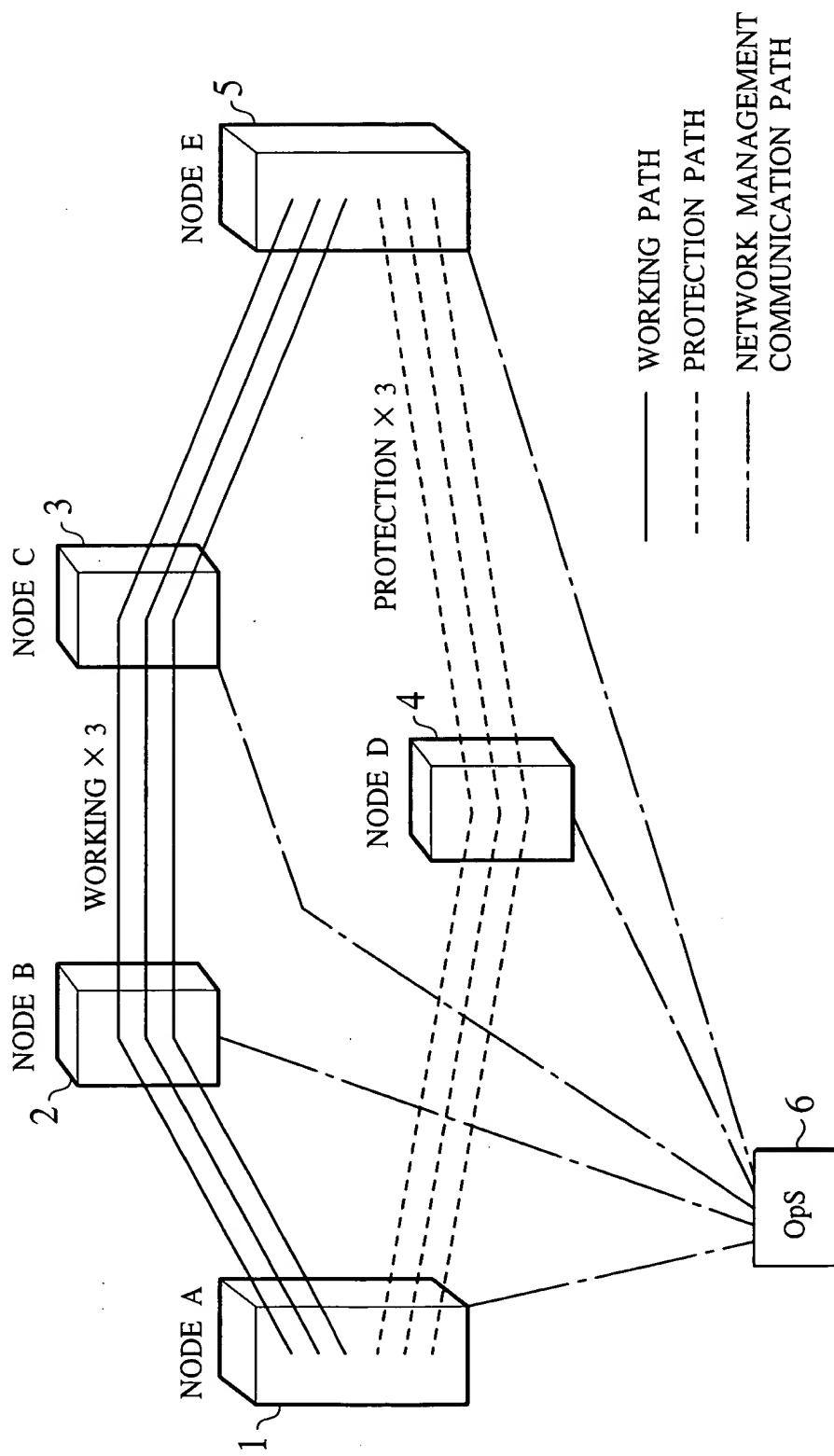


FIG.3
PRIOR ART

095096937-432600

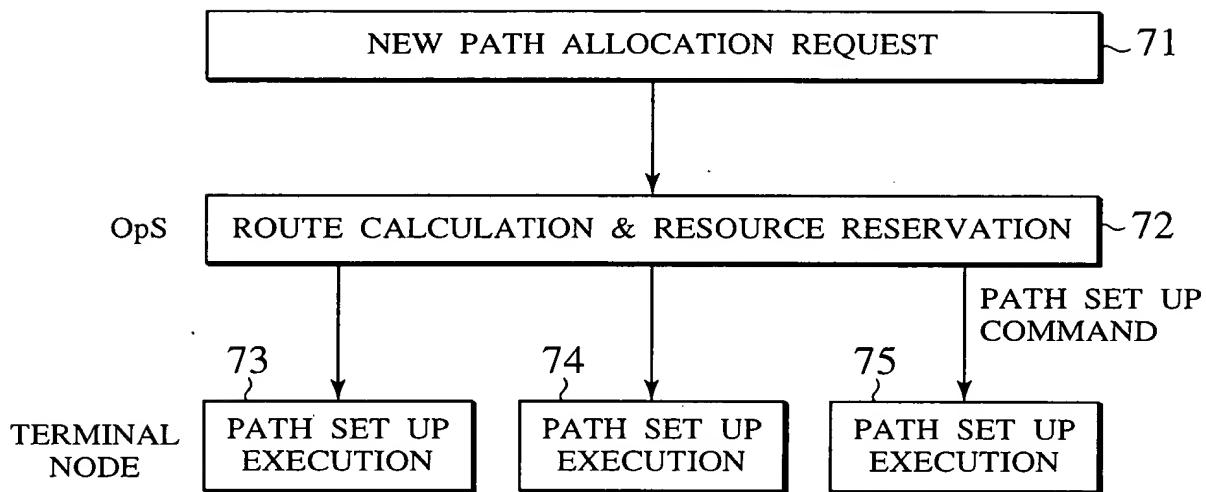


FIG.4

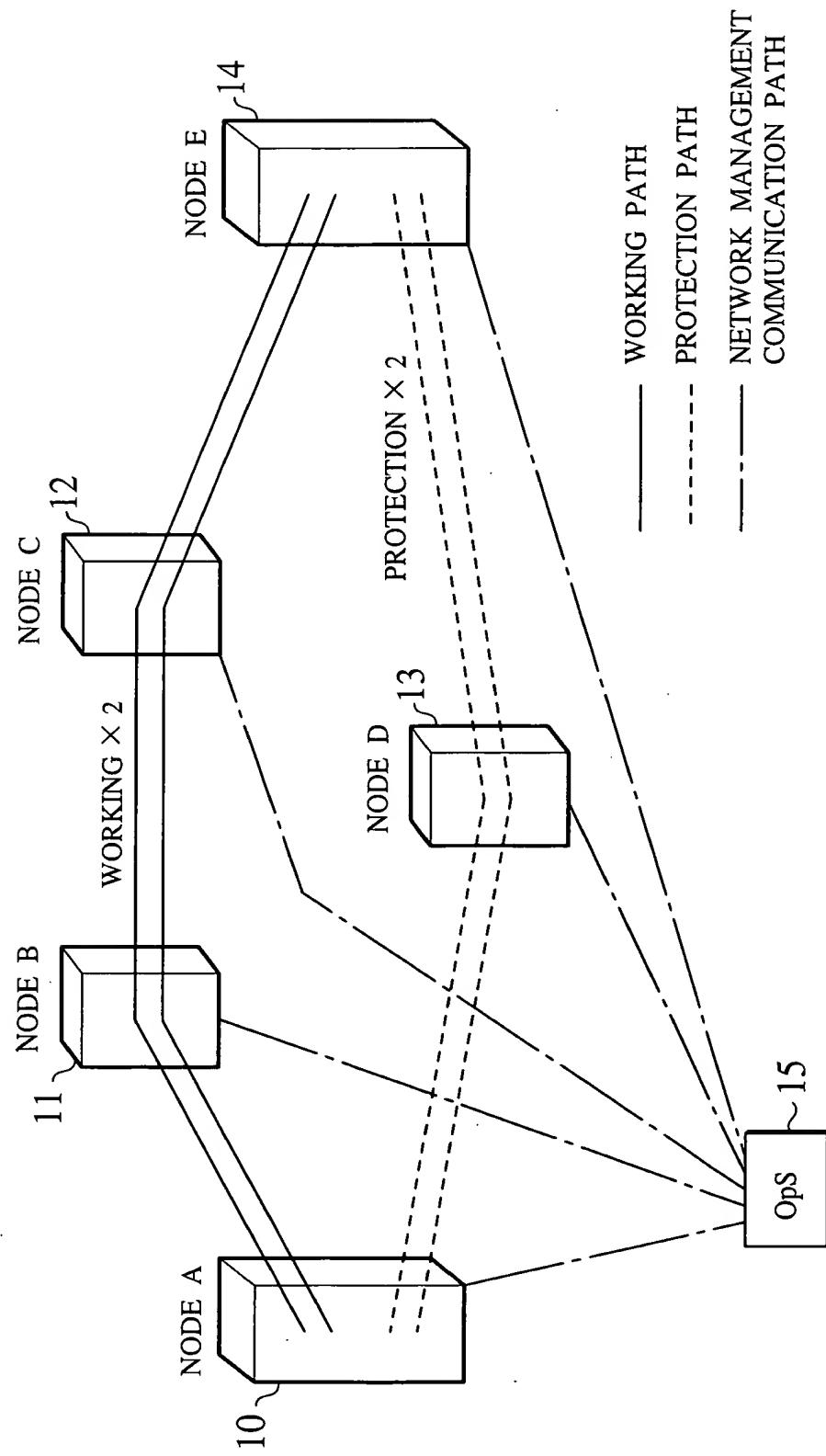


FIG.5

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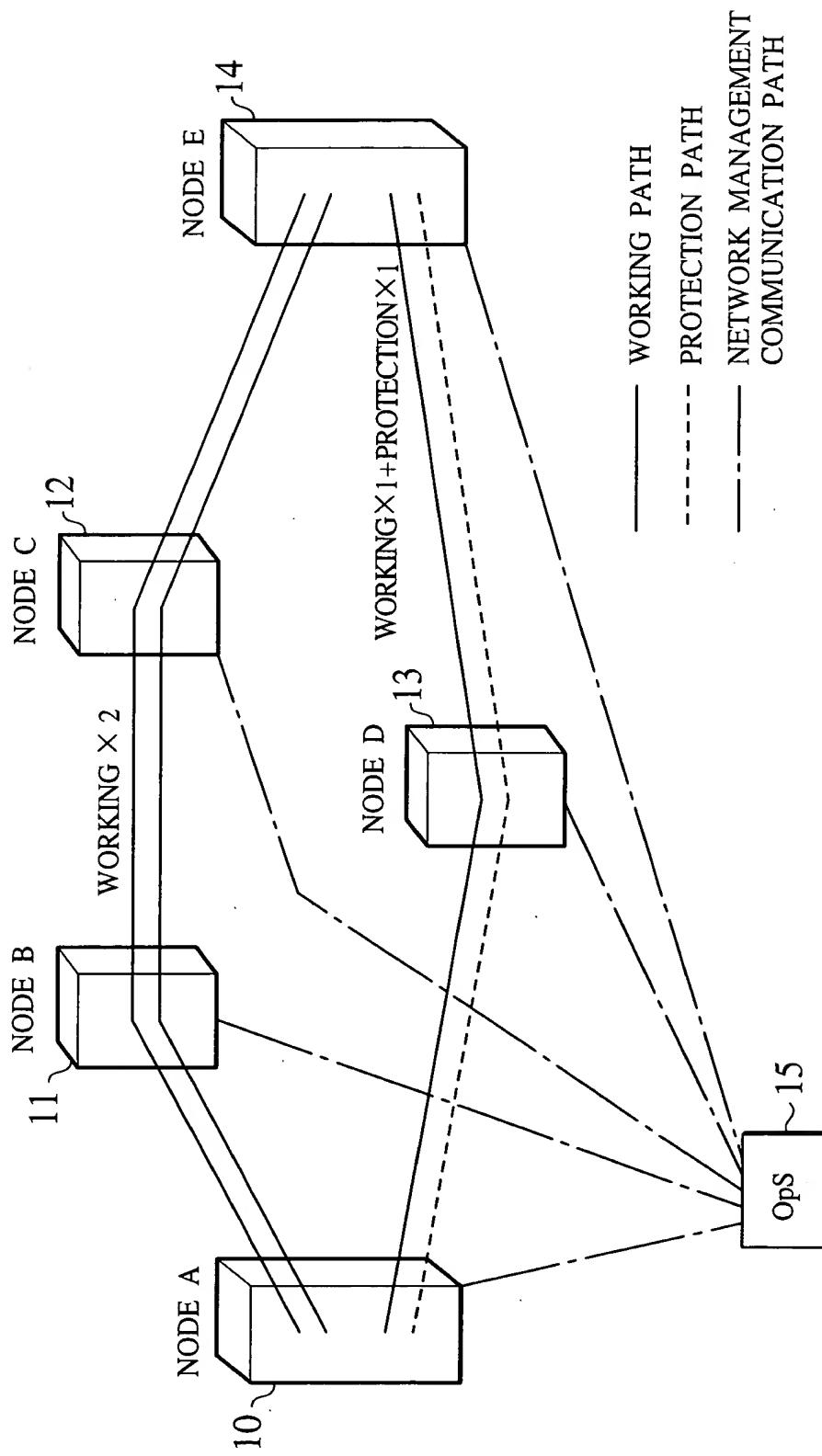


FIG. 6

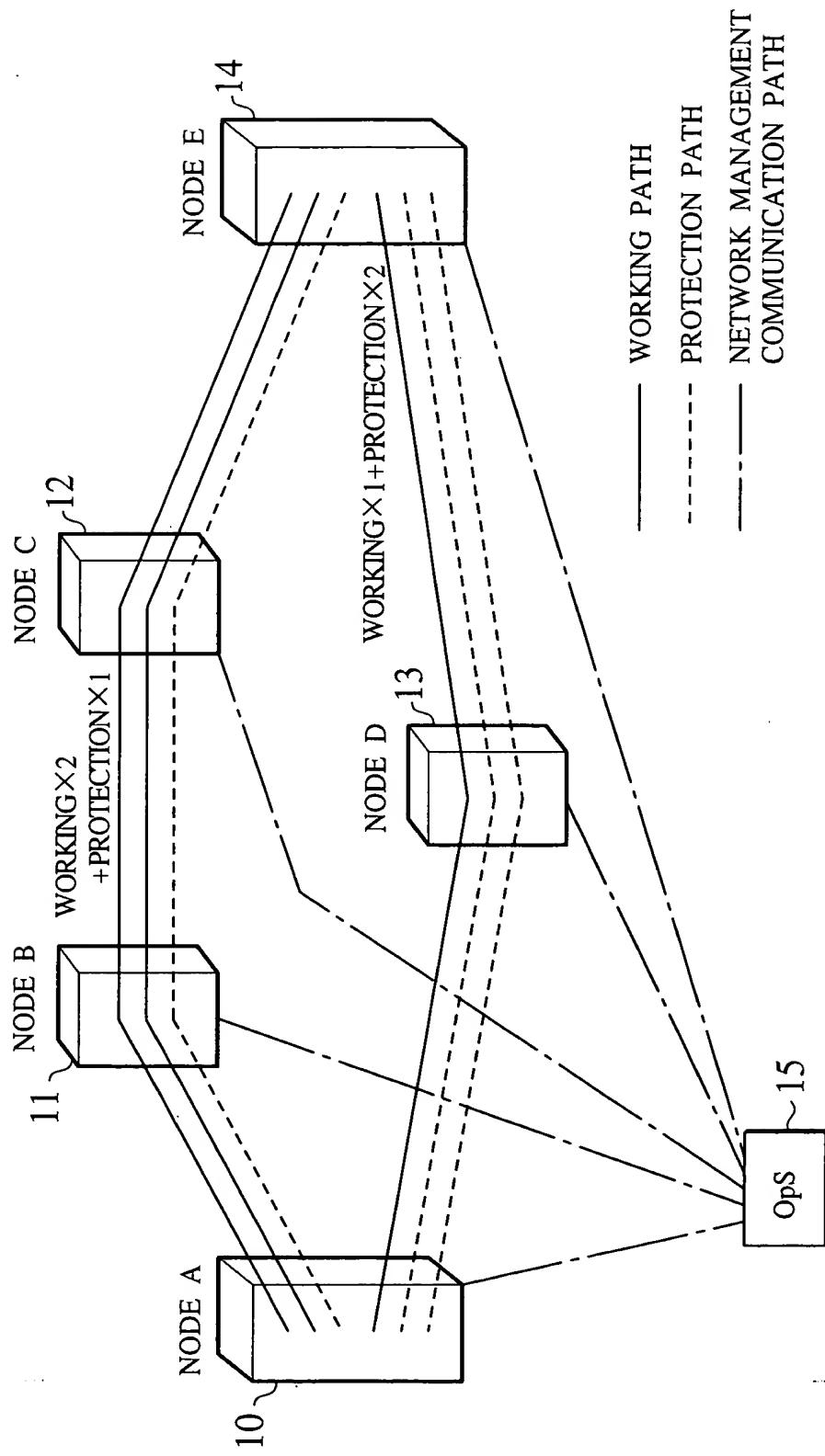


FIG.7

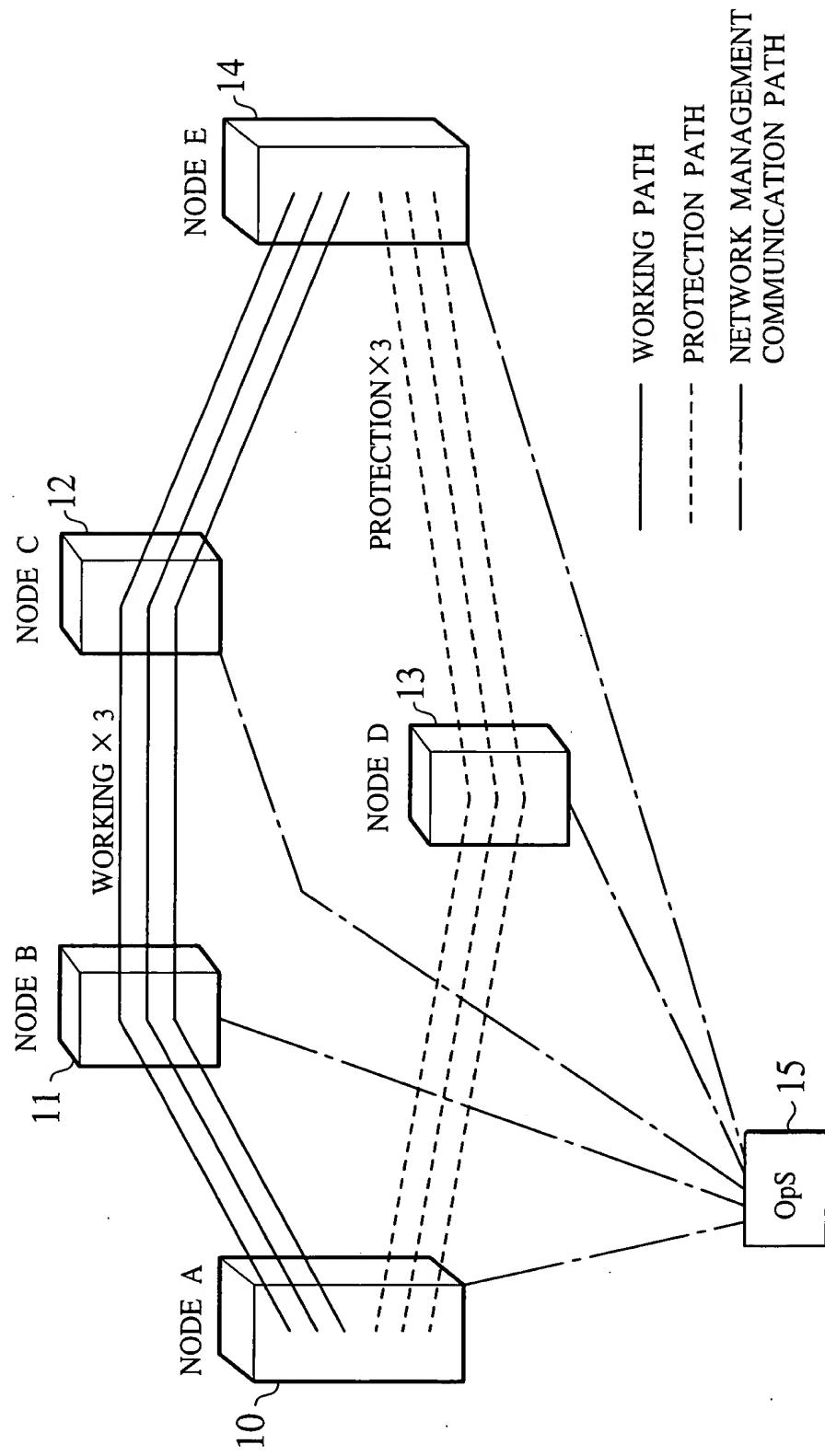


FIG.8

0000000000000000

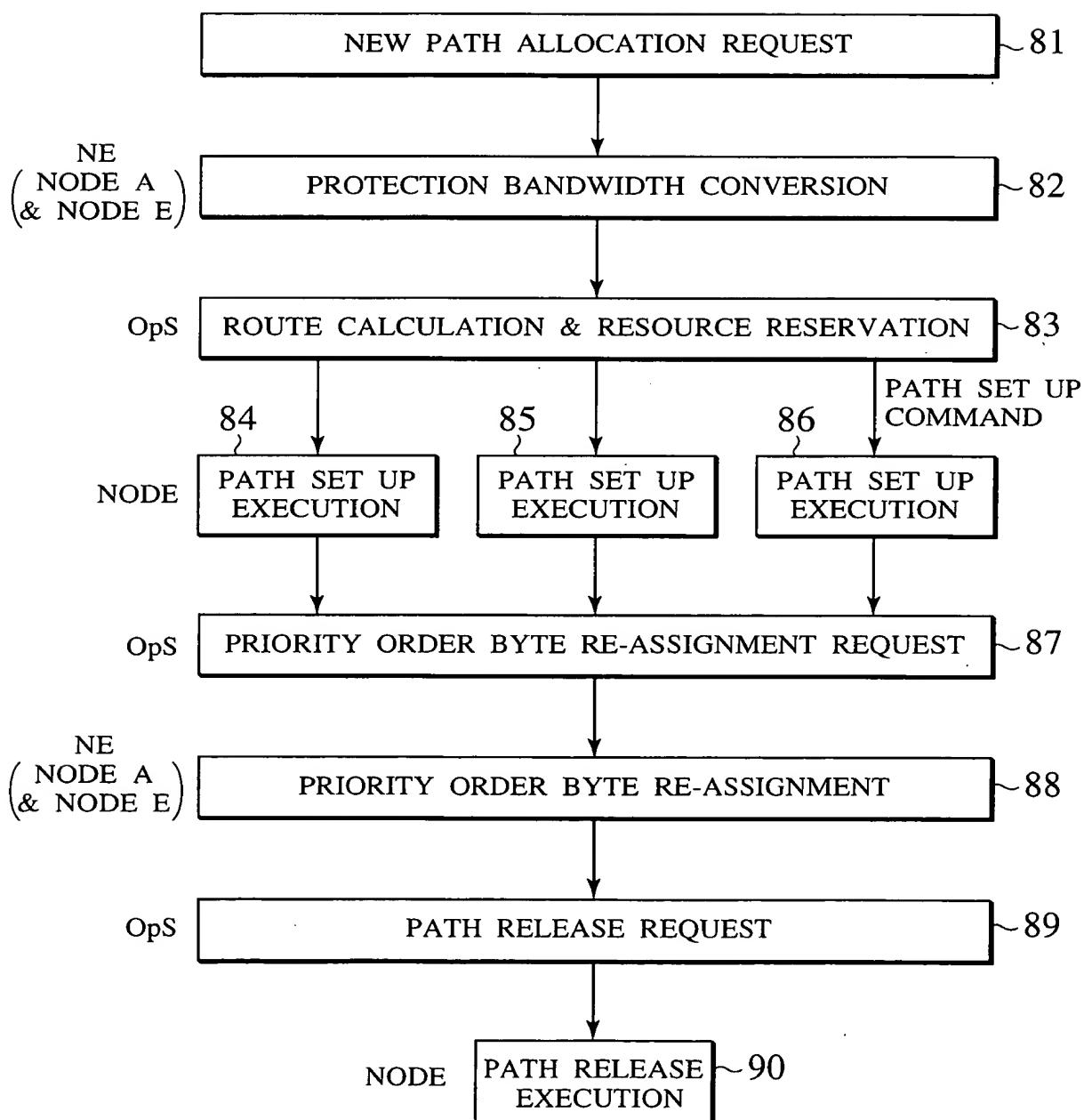


FIG. 9

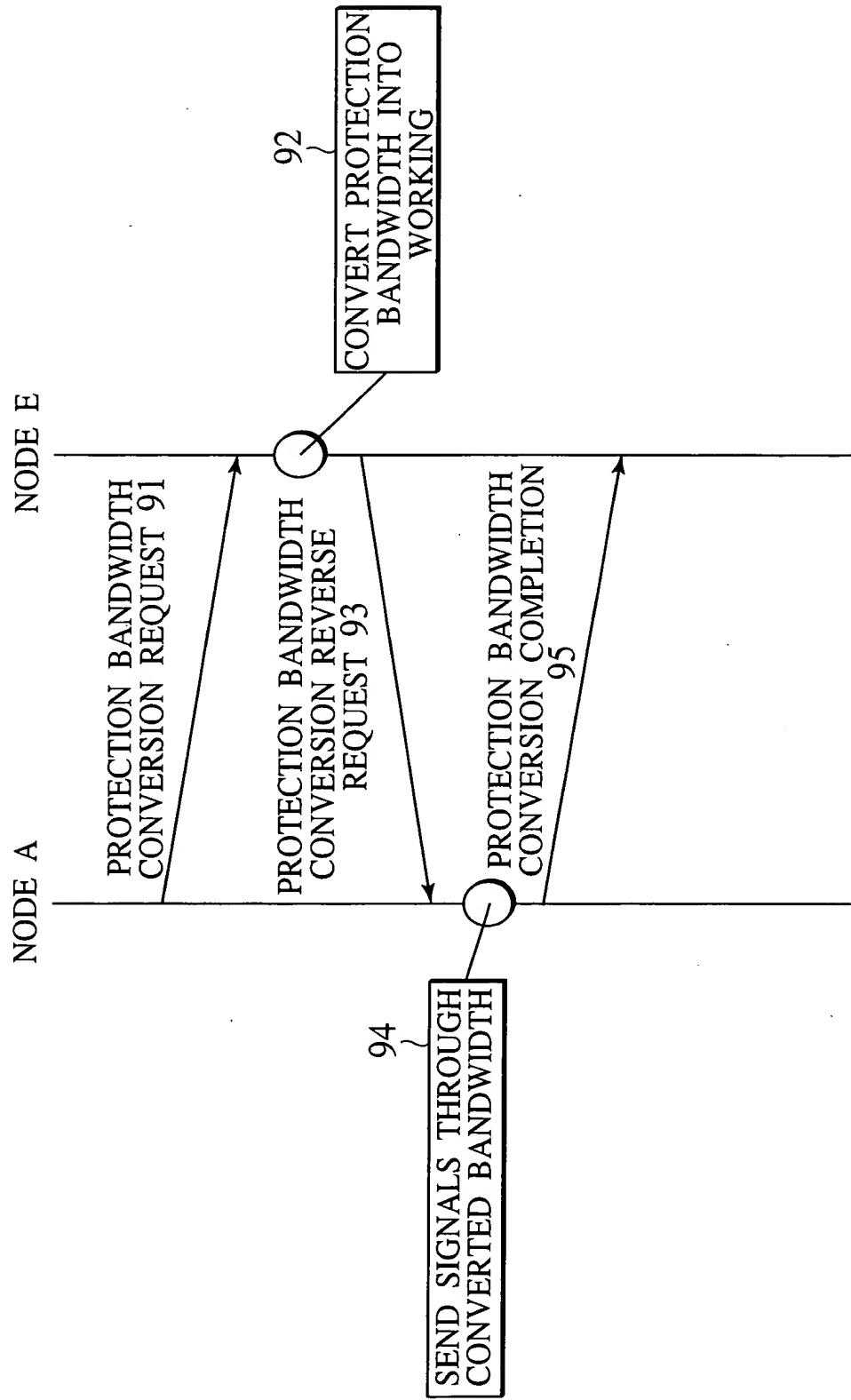


FIG.10

| BIT 1~4 | MEANING |
|---------|--|
| 0000 | ORDINARY STATE |
| 0001 | PROTECTION PATH CONVERSION REQUEST |
| 0011 | PROTECTION PATH CONVERSION REVERSE REQUEST |
| 0100 | PROTECTION PATH CONVERSION COMPLETE & WAITING FOR CHANGE |
| 0101 | PROTECTION PATH CONVERSION COMPLETE & WAITING FOR CHANGE (REPLY) |
| 0110 | CHANGING PRIORITY ORDER BYTE |
| 0111 | CHANGING PRIORITY ORDER BYTE (REPLY) |
| OTHERS | UNDEFINED |

FIG.11

| BIT POSITION | MEANING |
|--------------|---------------------------------------|
| BIT 1 | 1 : WORKING PATH, 0 : PROTECTION PATH |
| BIT 2~8 | PRIORITY ORDERS FROM 0 TO 127 |

FIG.12

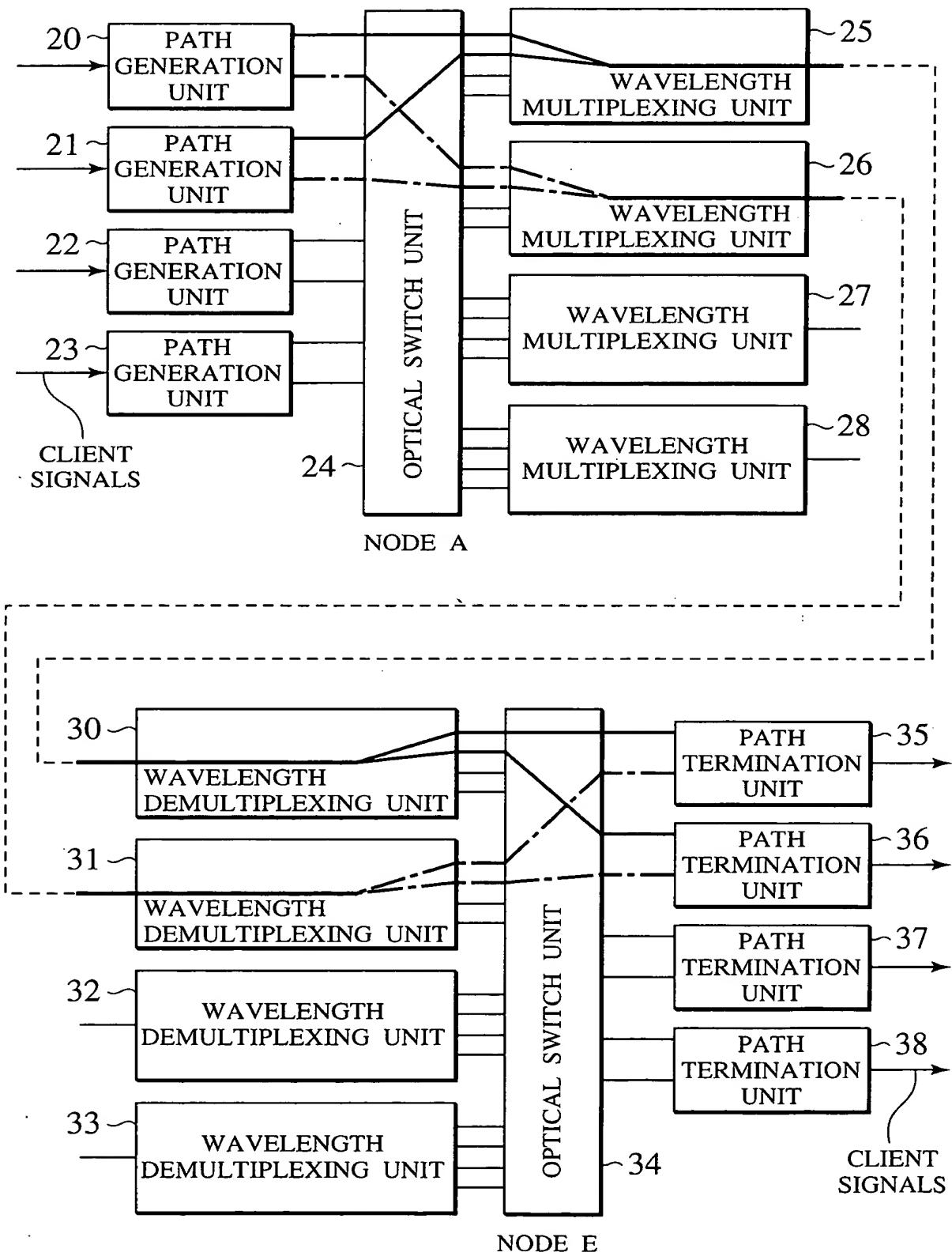


FIG.13

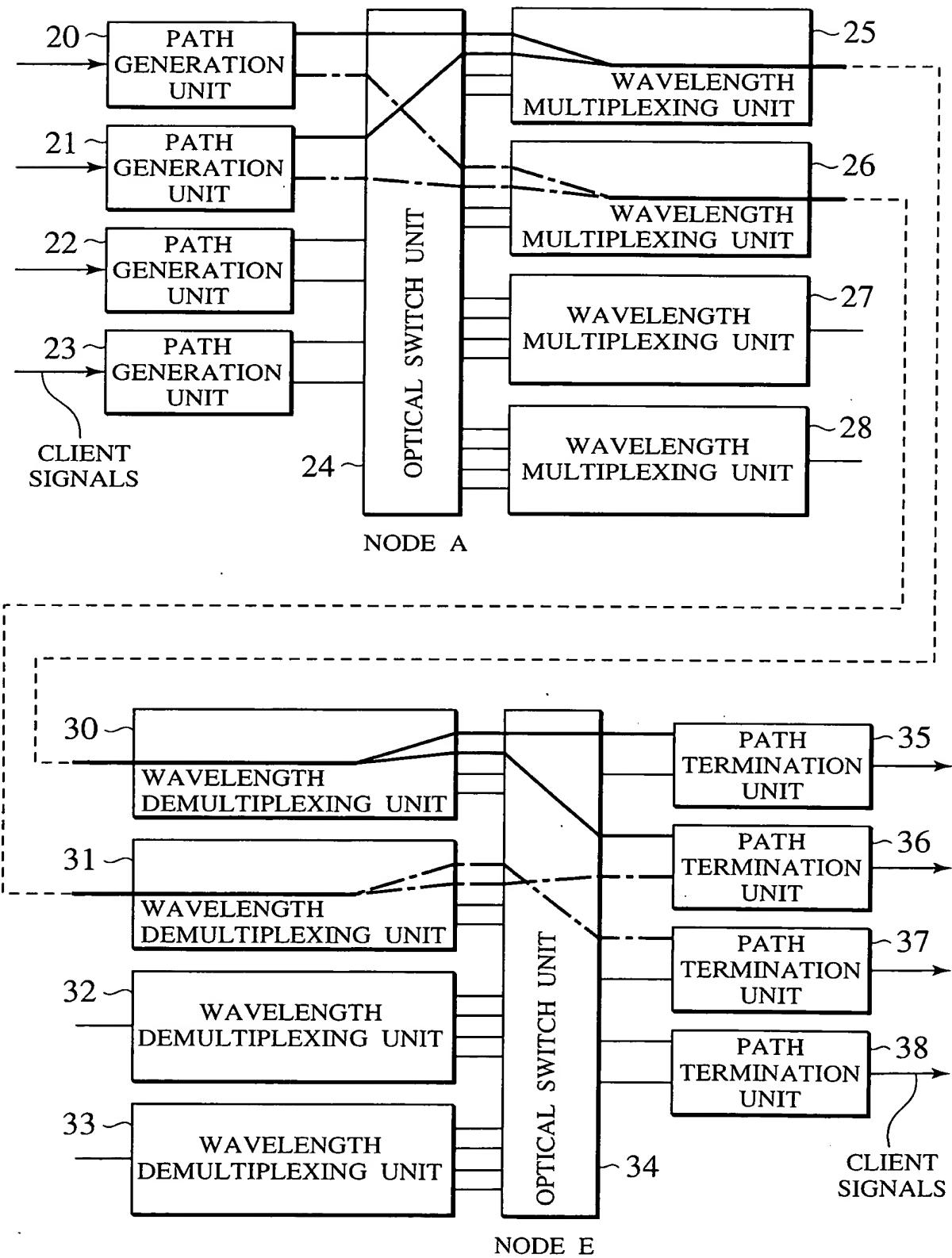


FIG.14

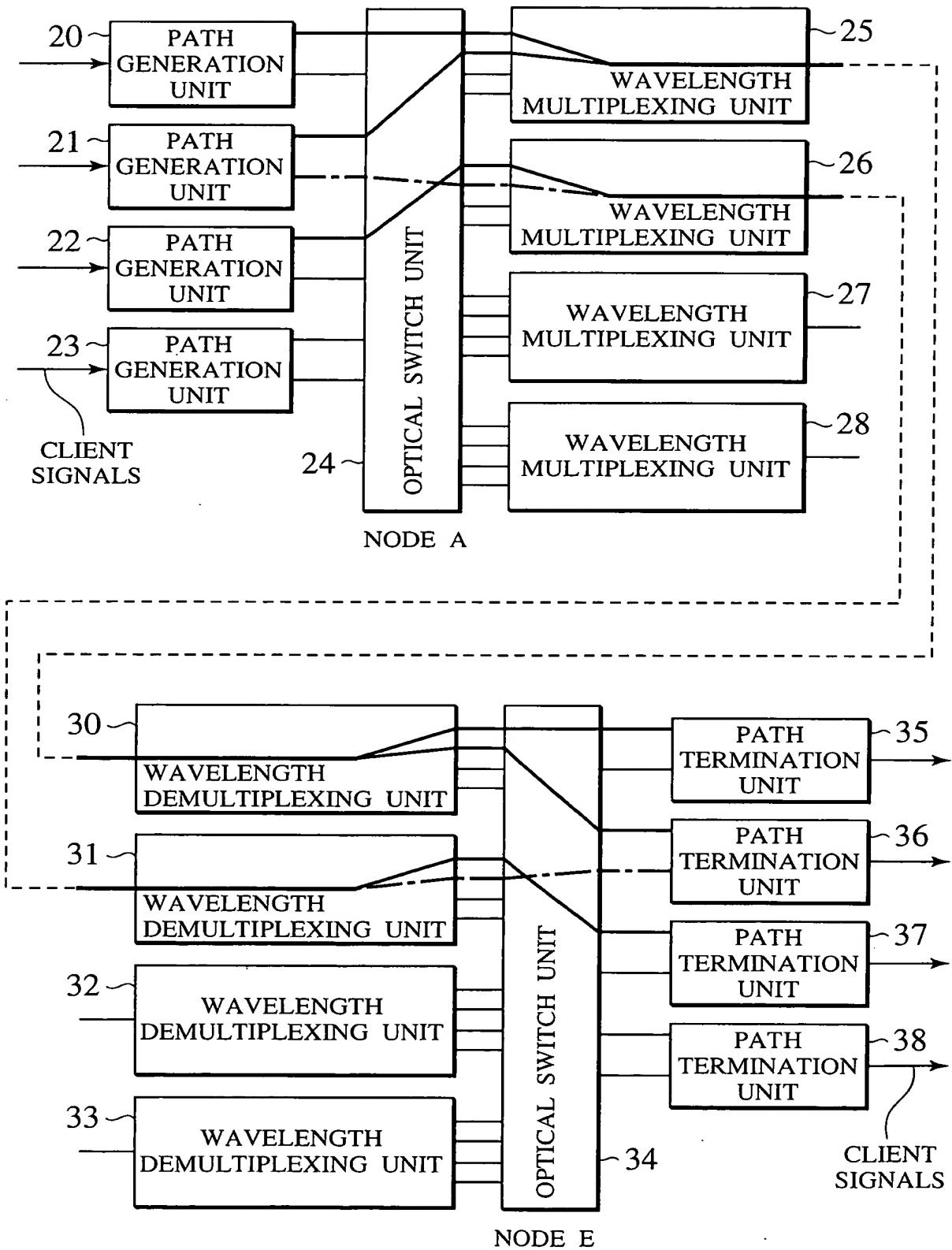


FIG.15

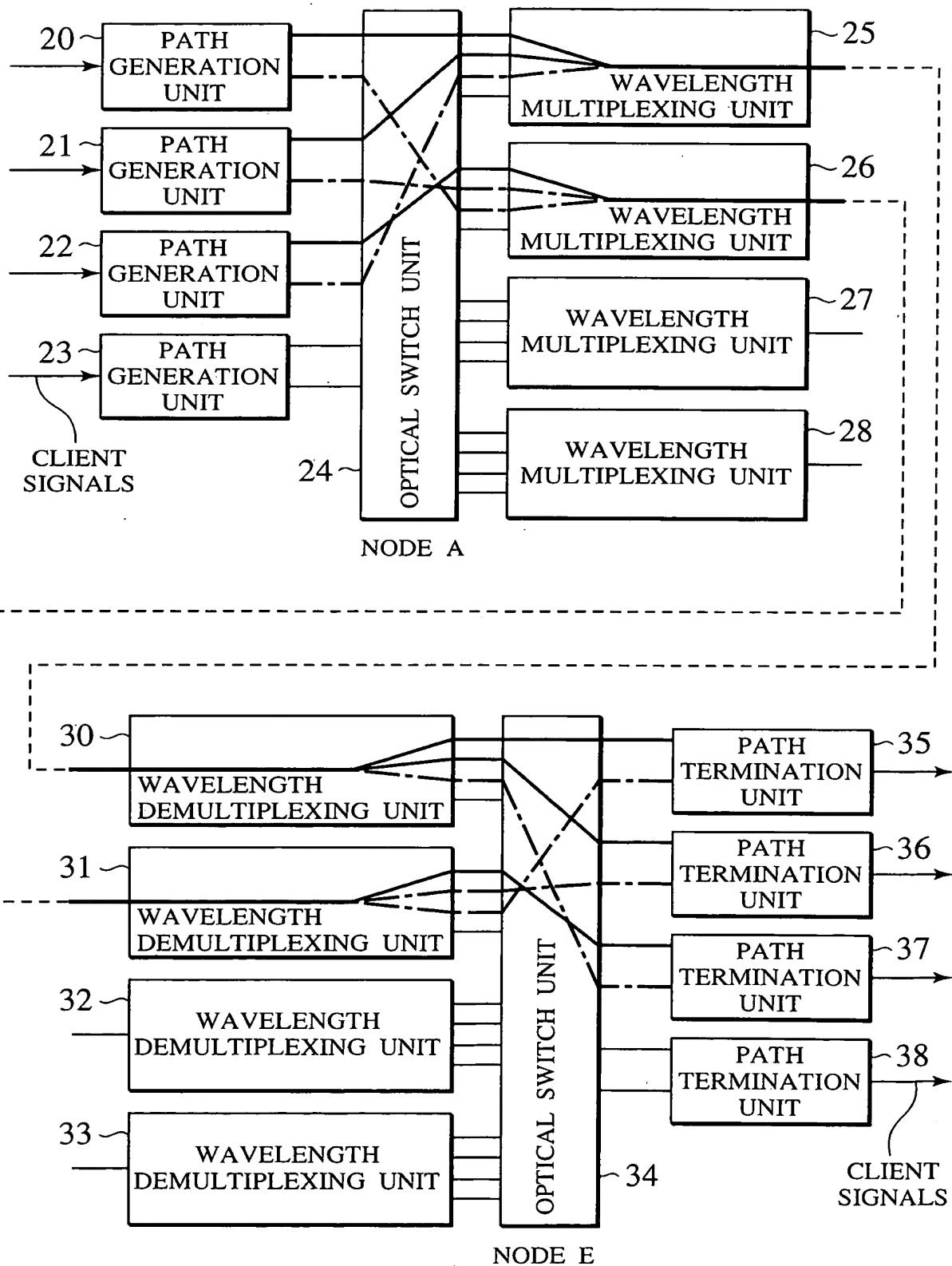


FIG.16

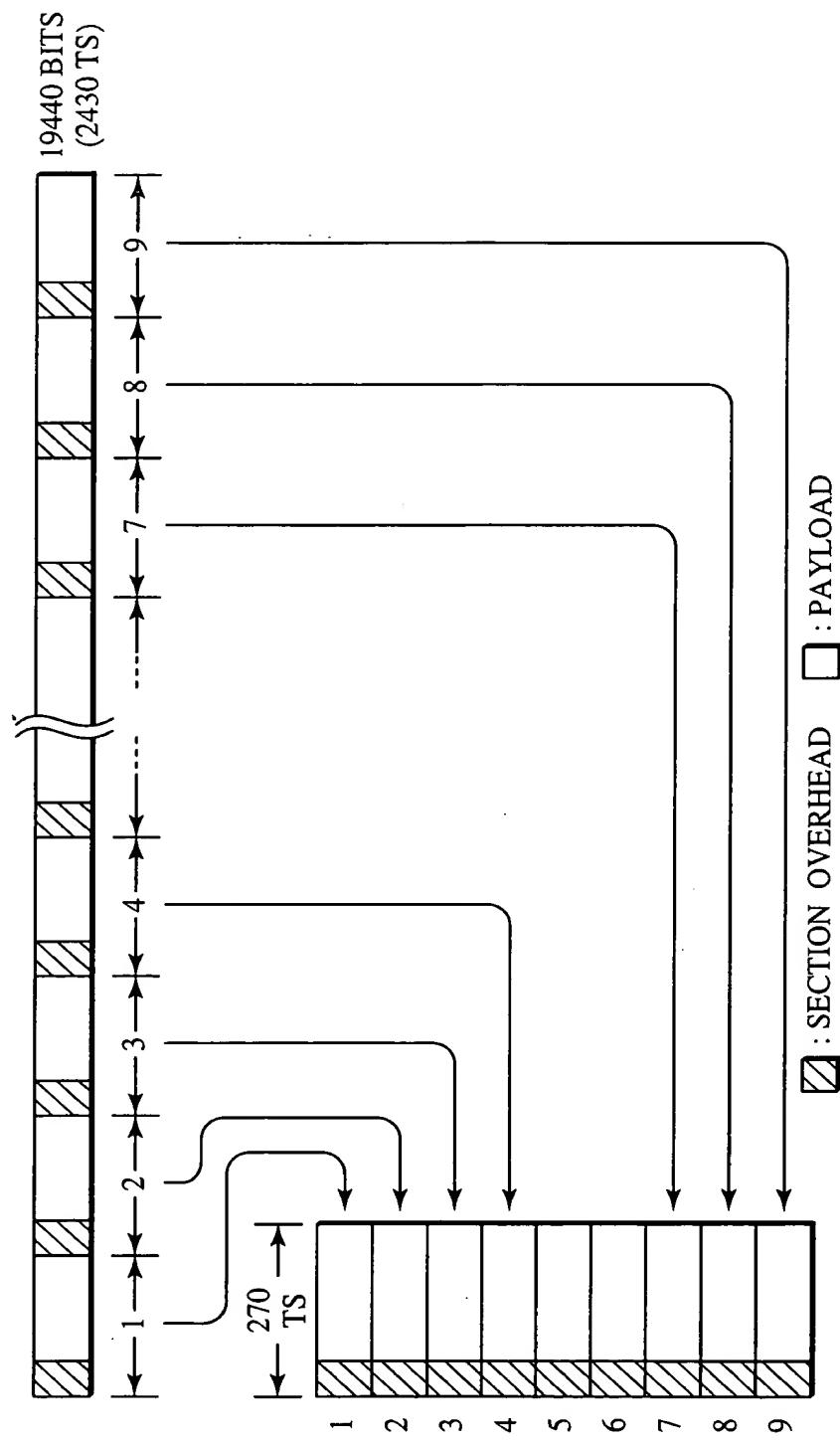


FIG.17

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